

STUDY AND EVALUATION SCHEME FOR DIPLOMA PROGRAMME IN

SIXTH SEMESTER (ELECTRONICS ENGINEERING)

Sr. No	Subject	L	T	P	T O T	EVALUATION SCHEME						Total Marks
						Internal Assessment		External Assessment (Examination)				
						Theory	Practical	Theory		Practical		
						Max. Marks	Max. Marks	Max. Marks	Hrs	Max. Marks	Hrs	
6.1	Optical Fiber Communication	3	1	2	6	25	20	75	2.5	40	3	160
6.2	Mobile Communication	4	0	3	7	25	20	75	2.5	40	3	160
6.3	Microcontroller and Embedded System	4	0	4	8	25	20	75	2.5	40	3	160
6.4	Computer Networks	4	0	3	7	25	20	75	2.5	40	3	160
6.5	Programmable Logic Controller	4	0	4	8	25	20	75	2.5	40	3	160
6.6 **	Employable Skills	-	-	4	4	-	25	-	-	-	-	25
6.7	Major Project	-	-	4	4	-	50	-	2.5	75	-	125
General Proficiency #		-	-	4	4	-	25	-	-	-	-	25
Industrial Exposure (Assessment at Inst. Level) +		-	-	-	-	-	25	-	-	-	-	25
Total		19	1	28	48	125	225	375	-	275	-	1000

** Common with other Engineering diploma programmers.

General Proficiency will comprise of various co-curricular activities like games, hobby clubs, seminars, declamation contests, extension lectures, NCC, NSS and cultural activities etc.

+ Industrial visit compulsory to minimum 2 Industries or Departments.

Note: 1- Each period will be 50 minutes.
2- Each session will be of 16 weeks.
3- Effective teaching will be at least 12.5 weeks.

